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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/634,988	08/06/2003	Mitsumi Ito	61282-035	7469	
7590 02/10/2005 MCDERMOTT, WILL & EMERY 600 13th Street, N.W.			EXAMINER		
			WHITMORE, STACY		
	C 20005-3096		ART UNIT	PAPER NUMBER	
			2825		
			DATE MAILED: 02/10/2009	DATE MAILED: 02/10/2005	

Please find below and/or attached an Office communication concerning this application or proceeding.

				H.H			
		Application No.	Applicant(s)				
Office Action Summary		10/634,988	ITO ET AL.				
		Examiner	Art Unit				
	•	Stacy A. Whitmore	2825				
Period f	The MAILING DATE of this communication or Reply	appears on the cover sheet wi	th the correspondence addres	ss			
THE - Extra after - If th - If N - Fail	HORTENED STATUTORY PERIOD FOR RE MAILING DATE OF THIS COMMUNICATIC ensions of time may be available under the provisions of 37 CFI r SIX (6) MONTHS from the mailing date of this communication e period for reply specified above is less than thirty (30) days, a O period for reply is specified above, the maximum statutory per ure to reply within the set or extended period for reply will, by start reply received by the Office later than three months after the mand patent term adjustment. See 37 CFR 1.704(b).	N. R 1.136(a). In no event, however, may a re I reply within the statutory minimum of thirt riod will apply and will expire SIX (6) MON atute, cause the application to become AB	eply be timely filed y (30) days will be considered timely. THS from the mailing date of this commu ANDONED (35 U.S.C. § 133).	ınication.			
Status							
1)[Responsive to communication(s) filed on 0	6 August 2003					
2a)□	This action is FINAL . 2b)⊠ This action is non-final.						
3) Since this application is in condition for allowance except for formal matters, prosecution as to the m							
,	closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
Disposi	tion of Claims						
5)□ 6)⊠ 7)⊠	Claim(s) <u>1-18</u> is/are pending in the applicate 4a) Of the above claim(s) is/are with Claim(s) is/are allowed. Claim(s) <u>1-8,13-15,17 and 18</u> is/are rejected Claim(s) <u>9-12 and 16</u> is/are objected to. Claim(s) are subject to restriction are	drawn from consideration.					
		avor election requirement.					
	tion Papers						
10)⊠	The specification is objected to by the Examination The drawing(s) filed on <u>06 August 2003</u> is/a Applicant may not request that any objection to Replacement drawing sheet(s) including the column The oath or declaration is objected to by the	re: a) accepted or b) ob the drawing(s) be held in abeyan rection is required if the drawing(ce. See 37 CFR 1.85(a). s) is objected to. See 37 CFR 1	` ,			
Priority	under 35 U.S.C. § 119						
a)	Acknowledgment is made of a claim for fore	nents have been received. The nents have been received in A periority documents have been reau (PCT Rule 17.2(a)).	pplication No received in this National Sta	ge			
Attachme	• •						
	ce of References Cited (PTO-892) ce of Draftsperson's Patent Drawing Review (PTO-948)		ummary (PTO-413))/Mail Date				
3) 🔲 Info	rmation Disclosure Statement(s) (PTO-1449 or PTO/SB er No(s)/Mail Date		formal Patent Application (PTO-152	2)			

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DETAILED ACTION

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1. The drawings are objected to because the drawings contain text that is barely legible. Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. The figure or figure number of an amended drawing should not be labeled as "amended." If a drawing figure is to be canceled, the appropriate figure must be removed from the replacement sheet, and where necessary, the remaining figures must be renumbered and appropriate changes made to the brief description of the several views of the drawings for consistency. Additional replacement sheets may be necessary to show the renumbering of the remaining figures. Each drawing sheet submitted after the filing date of an application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet" pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the

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international application designated the United States and was published under Article 21(2) of such treaty in the English language.

- 2. Claims 1, 4, and 13 are rejected under 35 U.S.C. 102(e) as being anticipated by Smith et al. (US Patent Application Publication 2003/0229875).
- 3. As for claims 1, 4, and 13, (claim 13 having similar limitations as claim 1), Smith discloses the invention as claimed, including a method of generating a pattern for a semiconductor device comprising:

A step of designing and arranging a layout pattern of a semiconductor chip [pg. 3, paragraph 0026, see also figs. 7, 9a, 13a, 13c, 16, especially element 34-1, 18, 23, 24, 28];

A step of extracting an area ratio of the layout pattern [pg. 3, paragraph 0026, see also figs. 7, 9a, 13a, 13c, 16, especially element 34-1, 18, 23, 24, 28]; and A sterp of adding and arranging a dummy pattern to the layout pattern, while consideration is given to the most appropriate area ratio of the layout pattern of the layer obtained according to a design rule of the layer composing the layout pattern, so that the area ratio of the layer can be the most appropriate ratio [pg. 3, paragraph 0026, see also figs. 7, 9a, 13a, 13c, 16, especially element 34-1, 18, 23, 24, 28].

4. As for claim 4, Smith discloses Wherein an area ratio after the completion of forming the dummy pattern is calculated, it is judged whether or not the area ratio is in a range of a predetermined condition, and when the area ratio is not in the range of the predetermined condition, several of the dummy patterns are replaced and the most appropriate dummy pattern cell is selected [pg. 3, paragraph 0026, see also figs. 7, 9a, 13a, 13c, 16, especially element 34-1, 18, 23, 24, 28].

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

- (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 5. Claims 2-3, 5-8, 14-15, and 17-18 are rejected under 35 U.S.C. 103(a) as being unpatentable over Smith et al. (US Patent Application Publication 2003/0229875) in view of Asakawa (US Patent Application Publication 2002/0157076).
- As for claims 2-3, 5-8, 14-15, and 17-18, Smith further discloses dividing the 6. layout pattern, extracting an area ratio for each small region, adding a dummy pattern. preparing a plurality of types of dummy pattern cells, selecting a desired dummy pattern according to the area ratio of the small region, a wiring layer pattern, a diffusion layer. gate electrode, a well, and a well [pg. 3, paragraph 0026, see also figs. 7, 9a, 13a, 13c, 16, especially element 34-1, 18, 23, 24, 28; also figs. 1a-b, 2a-b, 5a-b, 6a-b, 11, 14-15, 22a-b, 23, paragraphs 0179, 0186, 0192-0201, 0206, 0215, 0235-0237, 0255, 0259, 02621.

Smith does not specifically disclose using a mask pattern and the actual device made from the design process.

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Asakawa discloses a semiconductor device formed by using a mask [paragraph 0037].

It would have been obvious to one of ordinary skill in the art at the time the invention was made to combine the teachings of Smith and Asakawa because both Smith and Asakawa are directed towards the design and manufacture of semiconductor devices through the use of dummy patterns, and adding Asakawa's use of masks to manufacture a semiconductor device would have allowed Smith's design to utilize well known techniques to manufacture the designed semiconductor after design was complete [see Asakawa, paragraph 0037].

- 7. Claims 9-12, and 16 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.
- 8. The following is a statement of reasons for the indication of allowable subject matter: The prior art of record fails to disclose either singularly or in combination the invention as claimed including a method of generating a pattern for a semiconductor device including at least a step of adjusting a layout in the vertical direction so that the dummy pattern cell can compose an MOS capacitor cell; or a semiconductor device wherein the pattern for a semiconductor device has an aggregation of dummy patterns of the same size not to be electrically connected, and at least one of the dummy patterns on each layer includes a region overlapped with a dummy pattern on an upper or a lower layer of the layer concerned in the vertical direction.
- 9. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Stacy A. Whitmore whose telephone number is (571) 272-1685. The examiner can normally be reached on Monday-Thursday, alternate Friday 6:30am 4:00 pm.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew Smith can be reached on (571) 272-1907. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Stacy A Whitmore

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Primary Examiner

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